

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IAP12 REC d PCI/PTO 25 JUL 2006

Appl. No. : Unassigned Confirmation No. Unassigned
First Inventor : DEIXLER, Peter
Filed : Concurrently
TC/A.U. : Unassigned
Examiner : Unassigned

Docket No. : US030427US2
Customer No. : 24738

Title: Method of Fabricating a Sige Semiconductor Structure

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.